



2003
522-03

PATENT
Atty. Docket #: 197372US-2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :
Kazuhiro HASHIMOTO : GROUP ART UNIT: 2189
SERIAL NO: 09/664,856 : EXAMINER: K. Huynh
FILED: September 19, 2000 :
FOR: CIRCUIT, SYSTEM . . . :

RECEIVED

MAY 21 2003

Technology Center 2100

AMENDMENT

HONORABLE COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

Responsive to the Official Action of December 18, 2002, please amend the above-identified patent application as follows:

IN THE CLAIMS

Please amend Claim 1, 8 and 15 as shown in clean form below. A Marked-up copy is attached.

91 Sub 31
I. (Amended) A data transfer control circuit for carrying out data transfer by using a plurality of bus masters, comprising:

a data bus connected to a peripheral apparatus and including a plurality of unit data buses, into which the data bus is divided and through each of which data is transferred concurrently;

05/20/2003 NROHAMM1 00000022 09664856

01 FC:1202
02 FC:1201

108.00 OP
504.00 OP